ENEE 417 Fall 2002
Microelectronics Design Laboratory
Time Schedule: lectures W 09:00-09:50 EGR 1202
Laboratory: Tu 14:00-16:50 or M 09:00-11:50 or Th 14:00-16:50 or M 13:00-15:50 ; AVW 1330

Week beginning Wednesday	Week beginning Wednesday
09/04/02 Familiarize with equipment MAGIC tutorials Initiate Basic Paper choice	09/11/02 Basic Paper preliminary choice Transistor curve tracing a) Tektronix curve tracer; b) Spice c) Op-amp curve trace; printer printout
09/18/02 Final Basic Paper choice Learn MAGIC and layout techniques Finish op-amp curve tracer	09/25/02 Parts List for Basic Paper VLSI Layout Ring oscillator VLSI layout
10/02/02 CMOS Vthreshold or bandgap V determination circuit; investigation of super capacitors Oscillator, current mirror or differential pair choice	10/09/02 Finish Vthreshold or bandgap V determination circuit Use of analog programmable arrays
10/16/02 Oscillators, Current Mirrors; Differential Pairs Automated testing Experiments	10/23/02 Finish chosen oscillator design, measurements Automated testing
10/30/02 Base paper designs	11/06/02 RWN to IECON Basic paper designs
11/13/02 Basic paper measurements	11/20/02 Base paper measurements
11/27/02 Oral Reports	12/04/02 RWN to ICARCV Oral Reports
12/11/02 Last Class Written Reports Due	

| Th 12/19/02
Final Exam Scheduled
CHE 2108 08:00-10:00 | |