ENEE 417 - Fall 2001
Week #4
VLSI Layout

During this week you should:
1. Learn MAGIC layout. There are samples and tutorials now on the web at
        http://www.ee.umd.edu/newcomb/mslab-vlsi.html

2. Make a layout for an inverter and a three stage ring oscillator using your inverter.
3. Do a Spice extraction and
4. Verify your layout by Spice simulation.

Besides these, the key thing to be done is to obtain a base paper. You should also have finished your op-amp curve tracer.