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RWN 08/26/01- 12/05/01

ENEE 417 Fall 2001

Microelectronics Design Laboratory

Time Schedule: lectures W 09:00-09:50 CHE 2108

Laboratory: Tu 14:00-16:50 or M 09:00-11:50 or Th 14:00-16:50 or M 13:00-15:50 ; AVW 1330

Week beginning Wednesday	Week beginning Wednesday
08/29/01 Familiarize with equipment MAGIC tutorials Initiate Basic Paper choice	09/05/01 Basic Paper preliminary choice Transistor curve tracing a) Tektronix curve tracer; b) Spice c) Op-amp curve trace; printer printout
09/12/01 Learn MAGIC and layout techniques Final Basic Paper choice Finish op-amp curve tracer	09/19/01 VLSI Layout Ring oscillator VLSI layout
09/26/01 CMOS Vthreshold determination circuit  Oscillator, current mirror or differential pair choice	10/03/01 Finish Vthreshold determination circuit
10/10/01 Oscillators, Current Mirrors; Differential Pairs Web Based HP Experiments	10/17/01 Finish chosen oscillator design, measurements
10/24/01 Base paper designs	10/31/01 Basic paper designs
11/07/01 Basic paper measurements	11/14/01 Base paper measurements
11/21/01 Oral Reports	11/28/01 Oral Reports
12/05/01 Written Reports Due	12/12/01 exam study day no classes
F 12/14 Final Exam: 13:30-15:30 (if needed)	

