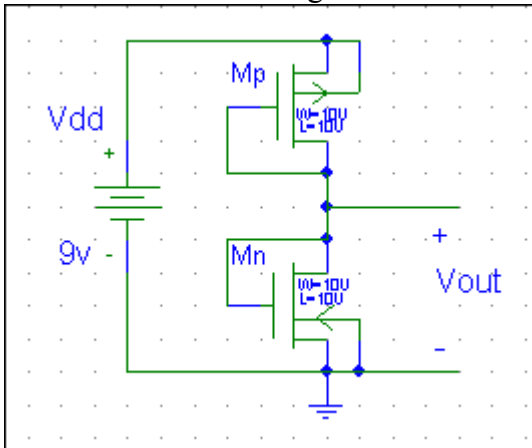


Open Book, Open Notes, 100 points, 60 minutes

Work all problems; if stuck or spending more than the indicated time, go on to the next problem. Be sure to sign your exam. Good luck.

1.(45 points, 25 min)

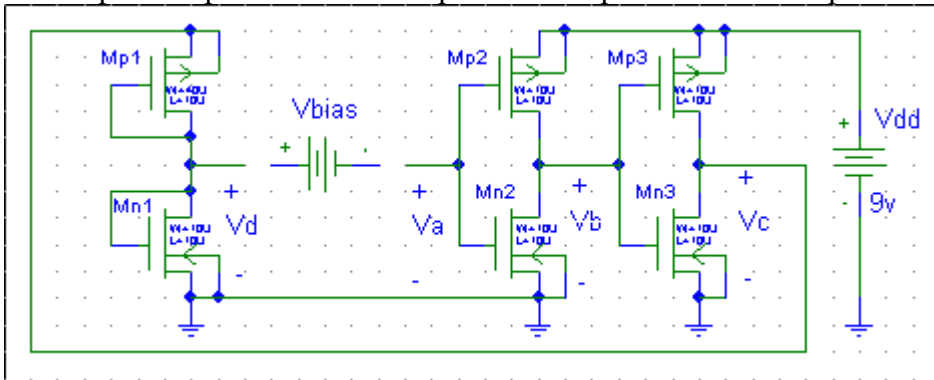
For the following circuit $K_{Pn}=2*K_{Pp}=2*10^{-4}a/v^2$, $V_{TOp}=-2*V_{TON}=-1v$



- Find V_{out} .
- If V_{dd} varies from 0v to 9v plot V_{out} versus V_{dd} .
- Assume all parameters are as originally specified, $V_{dd}=9v$, except that W_n is variable; find W_n such that V_{out} is 5v.

2. (55 points, 30 minutes)

For the following assume that $K_{Pp}=K_{Pn}=10^{-4}a/v^2$, $V_{TOp}=-V_{TON}=-1v$, $W_n=W_p=L_n=L_p=10u$ for all except that for M_{p1} the width is $W_{p1}=40u$.



- Sketch the transfer characteristic of the transistors, V_d versus V_a ; label important points. In so doing include sketches of the intermediate voltages V_b , V_c .
- When V_{bias} is connected (with short leads), sketch the resulting "load line" on the sketch of a) for various values of V_{bias} , distinguished by the number of intersect points. Indicate which of the intersect points are stable and which are unstable, giving your reasons.
- Replace V_{bias} by a PN diode, anode on the left, and sketch the load line on a sketch of a) and indicate stability of resulting intersections.