

ENEE 302 Final Exam Study Points - Update

I. Study the circuits listed under the following students:

1. James Campbell – not presented - Figure 1
2. David Greer – presented in second group - Figure 2
3. Avinash Narayana – presented in first group - Figure 4

II. Beside these also know how to design and determine the characteristics of BJT and MOS current sources, cascode current mirrors, inverters, and astable multivibrators made of CMOS NOR gates.

The final exam will be open book and will consist of two questions, one generic from II and I-1 above and the other chosen from the other two, presented, papers of I.

The notebooks, a collection of the dated and indexed work you have done for the course other than your term paper, are due after the final exam. Please turn these in to Saroj, Room AVW 2406.