

ENEE 302 Final Exam Study Points

I. Study the circuits listed under the following students according to the group you were in as well as the one not presented:

1. James Campbell – not presented
2. William Dorsey – presented in first group
3. David Greer – presented in second group
4. Ling-Bing Kung – presented in second group
5. Avinash Narayana – presented in first group

II. Beside these also know how to design and determine the characteristics of BJT and MOS current sources, cascode current mirrors, inverters, and astable multivibrators made of CMOS NOR gates.

The final exam will be open book and probably consist of two or three questions, one generic from II above and others chosen from among the four papers of I.

The papers are due the last day of class - if I get stuck in Singapore please turn them in to Saroj, Room AVW 2406, or the TA or grader.

The notebooks, a collection of the dated and indexed work you have done for the course other than your term paper, are due after the final exam. Please turn these in to Saroj, Room AVW 2406.