

ENEE 302 & 397 - Fall 2000 Digital Electronics; Tu Th 3:30-4:45 Room CHM 0119  
Paper Choices

Student/present dates	Crouse/Section:Email address	Commentator for / by
Felix Adusei	fkadusei@wam.umd.edu	Mohamed Ainane Mohamed Ainane
U. Cam, A. Toker an H. Kuntman, "CMOS FTFN Realisation Based on Translinear Cells," Electronics Letters, Vol. 36, No. 15, July 20, 2000, pp. 1255 - 1256.		
Mohamed Ainane	mainane@wam.umd.edu	Felix Adusei Felix Adusei
M. K. Kazimierczuk and A. Massarini, "Feedforward Control of DC-DC PWM Boost Converter," IEEE Transactions on Circuits and Systems - I, Vol. 44, No. 3, February 1997, pp. 143 - 148.		
Olisa Anwah	oanwah@glue.umd.edu	Nicole Charles Nicole Charles
J. N. Babanezhad, "A 100-MHz, 50-Ohm, -45-dB Distortion, 3.3-V CMOS Line Driver for Ethernet and Fast Ethernet Networking Applications," IEEE Journal of Solid-State Circuits, Vol. 34, No. 8, August 1999, pp. 1044 - 1050.		
Erica Banks	embanks@glue.umd.edu	James Campbell Stephan Schmidt
S. H.-L. Tu and C. Tooumazou, "Low-Distortion CMOS Complementary Class E RF Tuned Power Amplifiers," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 5, May 2000, pp. 774 - 779.		
James Campbell	jamesjpc@wam.umd.edu	Ibrahim Tunalier Erica Banks
V. H. Champac, V. Avendanyo and M. Linares, "Bit Line Sensing Strategy for Testing for Data Retention Faults in CMOS SRAMs," Electronics Letters, Vol. 36, No. 14, July 6, 2000, pp. 1182 -1183.		
Nicole Charles	ncharles@wam.umd.edu	Olisa Anwah Olisa Anwah
A. M. Ismail and A.M. Soliman, "Novel CMOS Current Feedback Op-Amp Realization Suitable for High Frequency Applications," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 6, June 2000, pp. 918 - 921.		
Kevin Ciccarelli	kevincic@wam.umd.edu	Martin Hong Martin Hong
F. Svelto, M. Conta, V. Della Torre and R. Castello, "A Low-Voltage Topology for CMOS RF Mixers," IEEE Transactions on Consumer Electronics, Vol. 45, No. 2, 1999, pp. 299 - 309.		
Raymond K. Crum	rkajdi@glue.umd.edu	Chris DeWeese Chris DeWeese
T. Serrano-Gotarredona, B. Linares-Barranco and A. G. Andreou, "Bipolar/CMOS Current-Source Flip-Flop for Application in Neuro-Fuzzy Systems," Electronics Letters, Vol 35, No. 16, August 8, 1999, pp. 1326 - 1328.		
Chris De Weese		Raymond K. Crum Raymond K. Crum
C. Fioche, U. Gatti and F. Maloberti, "Design Issues on High-Speed Integration?? Track-and-Holds in BiCMOS Tehnology," IEE Proceedings on Circuits, Devices and Systems, Vol. 147, No. 2, April 2000, pp. 100 - 106.		
Paul Donohue	pdonohue@wam.umd.edu	Victor Hamilton Victor Hamilton
M. A. Margarit, J. L. Tham, R. G. Meyer and M. J. Deen, "A Low-Noise, Low-Poer VCO with Automatic Amplitude Control for Wireless Applications," IEEE Journal of Solid-State Circuits, Vol. 34, No. 6, June 1999, pp. 761 - 771.		
William M. Dorsey	wmdorsey@glue.umd.edu	John Glancy

		John Glancy
K. T. Lau, W. Y. Wang and K. W. Ng, "Adiabatic-CMOS/CMOS-Adiabatic Logic Interface Circuit," International Journal of Electronics, Vol. 87, No. 1, January 2000, pp. 27 - 32.		
Pouya Eftekhari	pouya@wam.umd.edu	Vala Shahabi
		Vala Shahabi
X. Wang and R. R. Spencer, "A CMOS Two-Path Tree Search Detectro," IEEE Journal of Solid-State Circuits, Vol. 35, No. 6, June 2000, pp. 816 - 825.		
Brian Fink	finkyman@aol.com	?? Keisha D. Flen
	bfink@glue.umd.edu	?? Keisha D. Flen
S.-S. Hwang, "Dual-Loop DLL-Based Clock Synchroniser," Electronics Letters, Vol. 36, No. 14, July 6, 2000, pp. 1173 -1174.		
Keisha D. Flen	kflen@glue.umd.edu	?? Brian Fink
		?? Brian Fink
???????L. S. Y. Wong, C. Y. Kwok and G. A. Rigby, "A 1-V CMOS D/A Converter with Multi-Input Floating-Gate MOSFET," IEEE Journal of Solid-State Circuits, Vol. 34, No. 10, October 1999, pp. 1386 - 1390.		
Jeremy Fogelman	jemhf@glue.umd.edu	Christina Takara
		Christina Takara
H. Shoeland and S. Mattisson, "A 100-MHz CMOS Wide-Band IF Amplifier," IEEE Journal of Solid-State Circuits, Vol. 33, No. 4, April 1998, pp. 631 - 634.		
Brian C. Genco	genco@glue.umd.edu	Ling-Bing Kung
		Ling-Bing Kung
S. Kavadias, "Offset-Free Column Readout Circuit for CMOS Image Sensors," Electronics Letters, Vol. 35, No. 24, November 25, 1999, pp. 2112 - 2113.		
Nicole C. Gavlak	ncgavlak@wam.umd.edu	Maggie Snowel
		Maggie Snowel
P. Roo, R. R. Spencer and P. J Hurst, "A CMOS Analog Timing Recovery Circuit for PRML Detectors," IEEE Journal of Solid-State Circuits, Vol. 35, No.1, January 2000, pp. 56 - 65.		
John Glancy	jglancy@wam.umd.edu	William M. Dorsey
		William M. Dorsey
I. M. Filanovsky, "An Input-Free VT Extractor Circuit Using a Series Connection of Three Transistors," International Journal of Electronics, Vol. 82, No. 5, 1997, pp. 527 - 532.		
David Greer	marc@wam.umd.edu	Matthew Staebler
		Matthew Staebler
D. Lachartre, "Switched-Current Low-Power Fast Addressable Analogue Memory," Electronics Letters, Vol. 31, No. 21, October 12, 1995, pp. 1808 - 1809		
Victor Hamilton	civis@glue.umd.edu	Paul Donohue
		Paul Donohue
S. Xiao and C. Toumazou, "Second Generation Single and Two-Step GaAs Switched-Current Cells," Electronics Letters, Vol. 30, No. 9, April 28, 1994, pp. 681 - 683.		
Harry Himawan	hhimawan@glue.umd.edu	John Hwang
		John Hwang
Changed to 11/02/00: S. Wu and B. Razavi, "A 900MHz/1.8Ghz CMOS Receiver for Dual Band Applications," Digest of Papers, 1998 IEEE International Solid-State Circuits Conference, February 6, 1998, pp. 124 - 125.		
Martin Hong	mhong@glue.umd.edu	Kevin Ciccarelli
		Kevin Ciccarelli
C. Lauterbach, W. Weber and D. Roemer, "Charge Sharing Concept and New Clocking Scheme for Power Efficiency and Electromagnetic Emission Improvement of Boosted Charge Pumps," IEEE Journal of Solid-State Circuits, Vol. 35, No. 5, May 2000, pp. 719 - 723.		
John Hwang	hwangj@wam.umd.edu	Harry Himawan

		Harry Himawan
J. A. T. M. van den Homberg, "A Universal 0.03-mm <sup>2</sup> One-Pin Crystal Oscillator in CMOS," IEEE Journal of Solid-State Circuits, Vol. 34, No. 7, July 1999, pp. 956 -961.		
Ling-Bing Kung	kung@wam.umd.edu	Brian C. Genco
		Brian C. Genco
W. A. Serdijn, J. C. Kuenen, J. Mulder, M. vande Gevel, A. C. van der Woerd and A. H. M. van Roermund, " Current-Efficient CMOS Transconductor for Coninuous-Time gm-C Filters," Electronics Letters, Vol. 33, No. 9, April 24, 1997, pp. 769 - 771.		
Gi Kwon	alice@wam.umd.edu	Honter Lin
		Honter Lin
K. W. Current, "Design of a Quaternary Latch Circuit Using a Binary CMOS RS Latch," 2000, pp. ??????		
Honter Lin	honter@wam.umd.edu	Gi Kwon
		Gi Kwon
G. Fikos and S. Siskos, "Analobe CMOS Vector Normalisation Circuit," Electronics Letters, Vol. 35, No. 25, December 9, 1999, pp. 2197 - 2198.		
Alan Lum	alum@wam.umd.edu	Richard Wilson
		Richard Wilson
M. Pandey and R. E. Bryant, "Exploiting Symmetry when Verifying Transistor-Level Circuits by Symbolic Trajectory Evaluation," IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 18, No. 7, July 1999, pp. 918 - 935		
Adam Lutz	alutz@wam.umd.edu	Rebecca Tang
		Rebecca Tang
C. De Cataldo, G. Palumbo and S. Stivala, "New CMOS Current Mirrors with Improved High-Frequency Response," International Journal of Circuit Theory and Applications, Vol. 21, 1993, pp. 443 - 450.		
Gregory S. Mercer	gsmerc@glue.umd.edu	Jacob Sheridan
		Jacob Sheridan
S. Lee and H. K. Yu, "Accurate High-Frequency Equivalent Circuit Model of Silicon MOSFETs," Electronics Letters, Vol. 35, No. 17, August 19, 1999, pp. 1406 - 1407.		
Brian Merrell	merell@eng.umd.edu	Vivek Thaker
		Vivek Thaker
W. C. Song, C. J. Oh, G. H. Cho and H. B. Jung, "High Frequency/High Dynamic Range CMOS VGA," Electronics Letters, Vol. 36, No. 13, June 22, 2000, pp. 1096 - 1098.		
Avinash Narayana	narayana@wam.umd.edu	Brandon Whalen
		Ibrahim Tunalier
T. Funabashi, K. Takagi, T. Tsukada, H. Nakamura and M. Hara, "An NMOS Microcomputer Peripheral Interface Unit Incorporating an Analog-to-Digital Converter," IEEE Journal of Solid-State Circuits, Vol. SC-15, No. 1, February 1980, pp. 38 - 43.		
Uvel A. Paul	goldu@glue.umd.edu	Stephen Schmidt
		Navid Rahini
S. Lindors, K. Halonen and M. Ismail, "A 2.7-V Elliptical MOSFET-Only gm C-OTA Filter," IEEE Transactions on Circuits and Systems - II, Vol. 47, No. 2, February 2000, pp. 89 - 95.		
Navid Rahini	nrahini@wam.umd.edu	Uvel A. Paul
		Bryan Stepp
M. Al-Nsour and H. S. Abdel-Aty-Zh\ohdy, "Simple Low Power Analoge MOS Voltage Adder," Electronics Letters, Vol. 35, No. 7, April 1, 1999, pp. 552 - 553.		
Stephen Schmidt	smschmid@wam.umd.edu	Erica Banks
		Uvel A. Paul
P. Mandal and V. Visvanathan, "Sef-Biasing of Folded Cascode CMOS Op-Amps," International Journal of Electronics, Vol. 87, No. 7, 2000, pp. 795 - 808.		
Vala Shahabi		Pouya Eftekhari
		Pouya Eftekhari

G. Giustolisi, G. Pamisano, G. Palumbo and T. Segreto, "1.2-V CMOS Op-Amp with a Dynamically Biased Output Stage," IEEE Journal of Solid State Circuits, Vol. 35, No. 4, April 2000, pp. 632 - 637.		
Jacob Sheridan		Gregory S. Mercer
		Gregory S. Mercer
S.-J. Lee, B. Kim and K. Lee, "A Novel High-Speed Ring Oscillator for Multiphase Clock Generation Using Negative Skewed Delay Scheme," IEEE Journal of Solid-State Circuits, Vol. 32, No. 2, February 1997, pp. 289 - 291		
Maggie Snowel	Maggie@wam.umd.edu	Nicole C. Gavlak
		Nicole C. Gavlak
Y. K. Seng and S. S. Rofail, "Design and Analysis of a +1V CMOS Four-Quadrant Analogue Multiplier," IEE Proceedings, Circuits, Devices and Systems, Vol. 145, No. 3, June 1998, pp. 148 - 154.		
Matthew Staebler	mtstaeb@wam.umd.edu	David Greer
		David Greer
P. Heim and M. A. Jabri, "MOS Cascode-Mirror Biasing Circuit Operating at Any Current Level with Minimal Output Saturation Voltage," Electronics Letters, Vol. 31, No. 9, April 27, 1995, pp. 690 - 691.		
Bryan Stepp	brystepp@wam.umd.edu	Navid Rahini
		Brandon Whalen
K. Mayaram, "Output Voltage Analysis for the MOS Colpitts Oscillator," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 2, February 2000, pp. 260 - 263.		
Christina Takara	takaracm@wam.umd.edu	Jeremy Fogelman
		Jeremy Fogelman
H. Barthelemy and A. Fabre, "A New Floating Controlled Resistance Operating in Class AB," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 1, January 2000, pp. 67 - 72.		
Rebecca Tang	tangors@wam.umd.edu	Adam Lutz
		Adam Lutz
G. Weil, W. L. Engl and A. Renz, "Integrated Pacemakers," IEEE Journal of Solid-State Circuits, Vol. SC-5, No. 2, April 1970, pp. 67 - 73.		
Vivek Thaker	vjthaker@wam.umd.edu	Brian Merrell
		Brian Merrell
L. Dai and R. Harjani, "CMOS Switched-Op-Amp-Based Sample-and-Hold Circuit," IEEE Journal of Solid-State Circuits, Vol. 35, No. 1, January 2000, pp. 109 - 113.		
Ibrahim Tunalier	itunalie@wam.umd.edu	Avinash Narayana
		James Campbell
J. Ramirez-Angulo, A. Torralba, R. G. Carvajal and J. Tombs, "Low-Voltage CMOS Operational Amplifiers with Wide Input-Output Swing Based on a Novel Scheme," IEEE Transactions on Circuits and Systems - I, Vol. 47, No. 5, May 2000, pp. 772 - 774.		
Brandon Whalen	bhalen@wam.umd.edu	Bryan Stepp
		Avinash Narayana
V. Riewruiat, K. Anuntahirunrat and W. Surakampontrorn, "A Class AB CMOS Square-Rooting Circuit," International Journal of Electronics, Vol. 85, No. 1, January 1998, pp. 55 -60.		
Richard Wilson	ralph@glue.umd.edu	Alan Lum
		Alan Lum
M. S. Kappes, "A 3-V CMOS Low-Distortion Class AB Line Driver Suitable for HDSL Applications," IEEE Journal of Solid-State Circuits, Vol. 35, No. 3, March 2000, pp. 371 - 376		