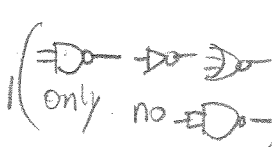


One Bit Full Adder

- Requirement:
1. Show Simplified Function Briefly
 2. Show logic gate graph according to $\begin{pmatrix} \text{only} & \text{no} \end{pmatrix}$ 
 3. Show Schematic graph by MOSFET according to 
 4. Show simulation result of 3
 5. Show LSI Layout of 3
 6. Analyse the result of 3 (Delay, Propagation time).

Note: 1. For 3: mention the size (W/L) of your transistors.

2. Advanced Requirement

- Compare difference (Delay, Propagation time) between different realizations (according different results of the simplified function)