1. Introduction to integrated electronic systems S&S Chapter 1
   a. I/O analysis
   b. Analog Systems
      i. Amplifiers
   c. Digital systems
      i. Logic gates
2. The OpAmp concept S&S Chapter 2
   a. Analog Systems Development with op amps
3. Discrete components – the passives S&S Chapter 3
   a. Resistors
   b. Diodes
   c. Capacitors
   d. Inductors
4. Actives I – MOSFETs S&S Chapter 4 (MI)
   a. How MOSFETs work
   b. Big signal/small signal models
   c. Quasi-static behavior
   d. Frequency response
   e. Introduction to MOS amps
   f. DC biasing
   g. MOS Logic
5. Actives II – Bipolars S&S Chapter 5
   a. How MOSFETs work
   b. Big signal/small signal models
   c. Quasi-static behavior
   d. Frequency response
   e. Bipolar amps
   f. DC Biasing
   g. BJT logic – current steering logic
6. The 4 Major amplifier topologies S&S Chapter 6 (MII)
   a. Common emitter/common base
   b. Followers
   c. Cascode
   d. Diff. pairs (S&S ch7.1, 7.2)
7. More on Digital Gates S&S Chapter 10
   a. RTL -> CMOS
   b. Switching times

MI = MID TERM I  MII = MID TERM II