Write, assemble and run successfully on the simulator a Mac-1 subroutine \texttt{lgneg(n,x)} that returns in the AC the address of the integer possessing the algebraically largest negative value along the real line among the \( n \) integers in the array whose starting address is \( x \). The largest negative value on the real line is the farthest right value to the left of zero. If there are no negative values among the \( n \) elements to be processed, then return -1 which is equivalent to the unsigned integer address 65535, clearly not a valid memory address. If there are two or more array entries that equally satisfy the requirements, return the address of the one with the highest address. Your subroutine should be tested with the main program shown below, which defines how the parameters are passed.

\[
\begin{align*}
\text{/main program} & \quad \text{/continued from below halt} \\
\text{EXTRN lgneg} & \quad \text{data 57} \\
\text{ans1} & \text{RES 1} \quad 0 \\
\text{ans2} & \text{RES 1} \quad 129 \\
\text{ans3} & \text{RES 1} \quad 34 \\
\text{n1} & \quad 6 \quad 8 \\
\text{n2} & \quad 10 \quad 3 \\
\text{n3} & \quad 5 \quad -29 \\
\text{start} & \quad \text{loco 4020} \quad -15 \\
\text{swap} & \quad \text{/initialize sp} \quad -2 \\
\text{loc} & \quad \text{n1} \quad -347 \\
\text{push} & \quad \text{/push address n1} \quad -3 \\
\text{loc} & \quad \text{data} \quad 6 \\
\text{push} & \quad \text{/push array start address} \quad 35 \\
\text{one} & \quad \text{call lgneg} \quad -413 \\
\text{stod ans1} & \quad \text{END} \quad \text{start} \\
\text{insp} & \quad 2 \\
\text{loc} & \quad \text{n2} \quad \text{/push address n2} \\
\text{push} & \quad \text{loc} \quad \text{data} \\
\text{add} & \quad (4) \\
\text{push} & \quad \text{/push array start address} \\
\text{two} & \quad \text{call lgneg} \\
\text{stod ans2} \\
\text{insp} & \quad 2 \\
\text{loc} & \quad \text{n3} \quad \text{/push address n3} \\
\text{push} & \quad \text{loc} \quad \text{data} \\
\text{add} & \quad (9) \\
\text{push} & \quad \text{/push array start address} \\
\text{three} & \quad \text{call lgneg} \\
\text{stod ans3} \\
\text{insp} & \quad 2 \\
\text{halt} \\
\text{/data array continues here but} & \\
\text{/ is shown in the above right hand column}
\end{align*}
\]

Hand in a copy of the main program symbolic assembly listing, the subroutine symbolic assembly listing, the contents of (macro) memory after “load main sub” (i.e., of main.abs) before execution of the program, and the contents of memory after execution of the program. Highlight and comment upon the final answers. Specify what values are contained in the addresses specified by \texttt{ans1}, \texttt{ans2}, and \texttt{ans3}.