Homework 2: ENEE 350
Due Date: March 6th 2007

Q1) Solve problem 3.12 from the book.

Q2) Solve Problem 3.9 from the book

Q3) Solve problem 3.7 from the book

Q4) Solve problem 4.11 from the book

Q5) Solve problem 4.9 from the book

Q6) Assuming you are given full adders, and gates, or gates not gates, multiplexers, xor gates, nand gates, nor gates etc. build a 4 bit ALU capable of working as an adder, subtracter, xor, and, or depending on the choice of the user. Explicitly mention how your ALU can be made to behave accordingly.