Q1) Write the MIPS assembly code for the following C code

You can assume that I,J and A(address for array A) are in registers s0,s1,s2. You can also assume that each data element in the array A is 4 bytes long.

Q2) Write the MIPS sequence of instructions for the following C code

Assume that b,c are in registers s0,s1. Which mathematical operation does this code implement.

Q3) Given a 32 bit binary sequence a31a30...a2a1a0 in a register say s0. I would like to extract the sequence ai...aj (with i>j, 0 < i <=32, 0 <= j < 32) and place them in the least significant bits of another register say t0. Write the sequence of MIPS instructions that allows one to do that. Also assume that i is available in register s1 and j is s2.

Q4) Give the advantages and disadvantages of having a very big register file as opposed to having a smaller one.