Problems 3.5, 3.7, 3.11 (note that disjunctive normal form is same as SOP form), 3.14, 3.18, 3.20

Question: For the gate networks shown in figure 3.20
   a) For figure 3.20a convert the gate network into another one which uses NAND gates only
   b) For figure 3.20b convert the gate network into another one with uses NOR gates only
Of course the implemented function should not change